Late Binding of Variables During Test Case Generation for Hardware and Software Design Verification

ABSTRACT OF THE DISCLOSURE

Methods and systems are provided that improve design verification by test generators by delaying assignment of values in the generated stimuli until these values are used in the design. Late binding allows the generator to have a more accurate view of the state of the design, and in order to choose correct values. Late binding can significantly improve test coverage with a reasonable performance penalty as measured by simulation time.

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